

Attorney's Docket No.: Intel 10559-880001
Client's Ref. No.: P17482

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Applicant : Yan Borodovsky
Serial No. : 10/688,337
Filed : October 17, 2003

Art Unit : 1765
Examiner : unknown
Assignee : Intel Corporation


Title : Composite Patterning with Trenches

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is an Information Disclosure Statement, faxed this 21st day of January, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: January 21, 2005



Scott C. Harris
Reg. No. 32,030
Attorney for Intel Corporation
By John F. Conroy
Reg. No. 45,485

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JFC/cla

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Attorney's Docket No.: Intel 10559-880001/P17482

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yan Borodovsky Art Unit: 1765
Serial No.: 10/688,337 Examiner: unknown
Filed : October 17, 2003 Assignee: Intel Corporation
Title : COMPOSITE PATTERNING WITH TRENCHES

VIA FACSIMILE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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JAN 21 2005

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information
Disclosure Statement and document listed on form PTO-1449.

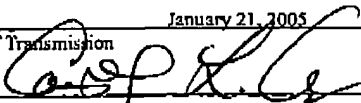
This filing is being made before the receipt of a first
Office action on the merits. No fee is required.

The documents are in the English language; hence no concise
explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the
return of a copy of the enclosed form PTO-1449 with the
Examiner's initials in the left column per MPEP 609 are
earnestly solicited along with an early action on the merits.

CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being transmitted by
facsimile to the Patent and Trademark Office on the date indicated
below.

January 21, 2005
Date of Transmission

Signature

Carroll Allman
Typed or Printed Name of Person Signing Certificate

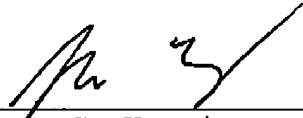
Page 2 of 2

Attorney's Docket No.: Intel 10559-880001 /
P17482

Please apply any additional charges or credits to Deposit
Account No. 06-1050.

Respectfully submitted,

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Sheet 1 of 1

Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-880001	Application No. 10/688,337
	Applicant Yan Borodovsky		
	Filing Date October 17, 2003	Group Art Unit 1765	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	"Gratings of regular arrays and trim exposures for ultralarge scale integrated circuit phase-shift lithography"; Fritze et al.; Journal Vacuum. Sci Technology B; Vol. 19, No. 6, pp. 1366-2370; Nov/Dec 2001
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Disclosure Form (PTO-1449)